

RV-8803-C7 Development Board

DEVELOPMENT BOARD



Temperature Compensated Real Time Clock / Calendar Module

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Development Board

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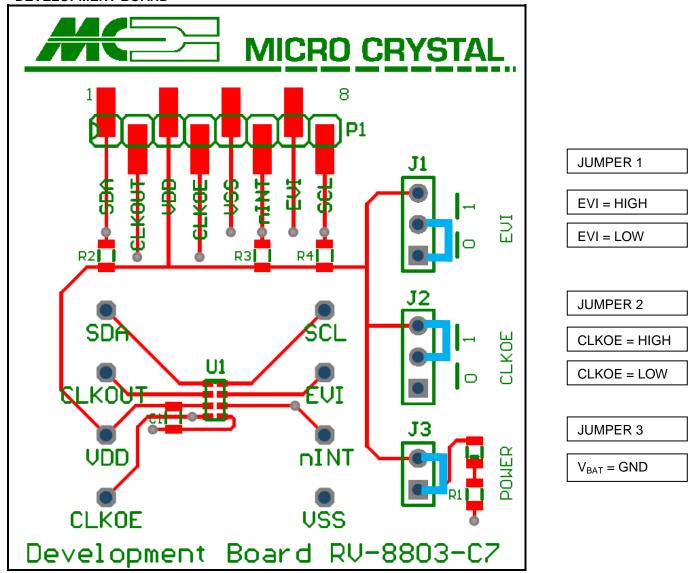
The RV-8803-C7 is soldered onto the Development Board.

Every pin is either accessible at test pins 1 - 8 or at the test vias situated around the device.

The following passive components are already soldered on the Board:

C1	10 nF	Decoupling capacitor between V _{SS} and V _{DD}
R1	330Ω	Current limiting resistor for LED
LED	green	Supply, current consumption of the LED has to be considered
R2	10 kΏ	Pull-up resistor SDA to V _{DD}
R3	10 kΏ	Pull-up resistor INT to V _{DD}
R4	10 kΏ	Pull-up resistor SCL to V _{DD}

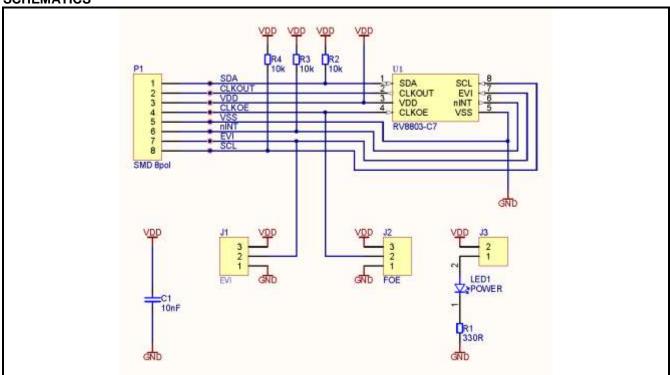
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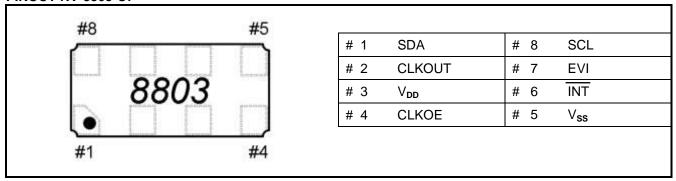
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SCHEMATICS



PINOUT RV-8803-C7



PIN DESCRIPTION

Symbol	Pin#	Description
SDA	1	Serial Data Input-Output pin; open-drain; requires pull-up resistor
CLKOUT	2	Clock Output; push-pull; controlled by CLKOE. If CLKOE is active HIGH, the CLKOUT pin drives the square wave of 32.768 kHz, 1.024 kHz or 1 Hz (Default value is 32.768 kHz). When CLKOE is tied to Ground, the CLKOUT pin is high impedance (tri-state).
V_{DD}	3	Positive supply voltage; positive or negative steps in supply voltage may affect oscillator performance, recommend 10 nF decoupling capacitor close to the device
CLKOE	4	Input to enable the CLKOUT pin. If CLKOE is active HIGH, the CLKOUT pin is in output mode. When CLKOE is tied to Ground, the CLKOUT pin is stopped and is high impedance (tri-state).
V _{SS}	5	Ground
ĪNT	6	Interrupt Output; open-drain; requires pull-up resistor; Used to output Alarm, Periodic Countdown Timer, Periodic Time Update and External Event Interrupt signals.
EVI	7	External Event Interrupt Input.
SCL	8	I ² C Serial Clock Input; open-drain; requires pull-up resistor.

Datasheet and Application-Manual are available for download under: www.microcrystal.com