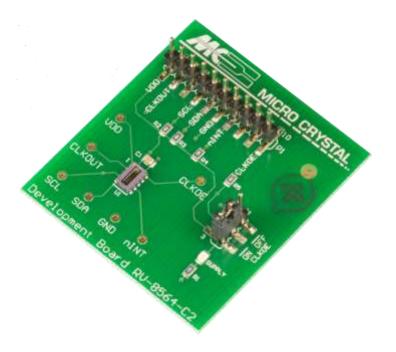


RV-8564-C2 Development Board

DEVELOPMENT BOARD



RV-0564-C2

Low-Power Real Time Clock / Calendar Module

DATE: April 2016

Revision No.: 2

Headquarters:

Page 1/3 Micro Crystal AG Muelestrasse 14 CH-2540 Grenchen Switzerland

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Development Board

RV-8564-C2

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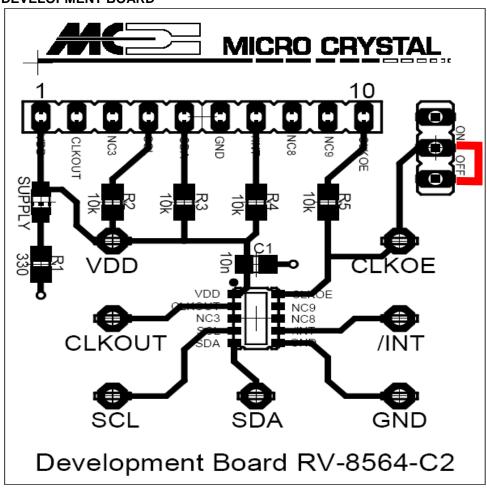
The RV-8564-C2 is soldered onto the Development Board.

Every pin is either accessible at test pins 1 - 10 or at the test vias situated around the device.

The following passive components are already soldered on the Board:

C1	10 nF	Decoupling capacitor between V _{SS} and V _{DD}
R1	330 Ώ	current limiting resistor for LED
LEI	D green	Supply, current consumption of the LED has to be considered
R2	10 kΩ	Pull-up resistor SCL to V _{DD}
R3	10 kΏ	Pull-up resistor SDA to V _{DD}
R4	10 kΩ	Pull-up resistor INT to V _{DD}
R5	10 kΩ	Protection resistor to prevent short-circuit between external CLKOE signal and Jumpe

DEVELOPMENT BOARD



JUMPER

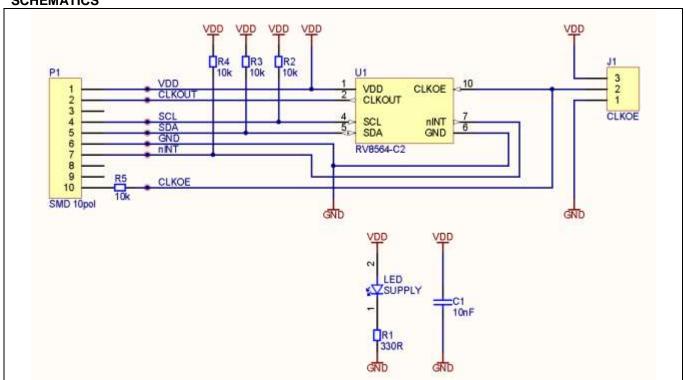
CLKOE = HIGH

CLKOE = LOW

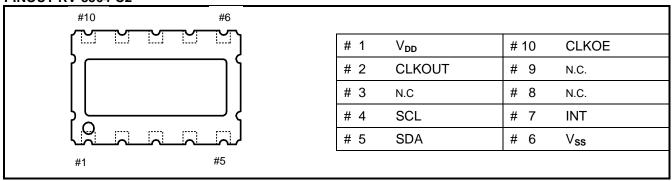
Development Board

RV-8564-C2

SCHEMATICS



PINOUT RV-8564-C2



PIN DESCRIPTION

Symbol	Pin#	Description
V_{DD}	1	Positive supply voltage; recommended 10 nF decoupling capacitor close to device
CLKOUT	2	Clock Output pin; push-pull output; at power-up by default 32.768kHz
NC	3	Not Connected
SCL	4	Serial Clock Input pin; requires pull-up resistor
SDA	5	Serial Data Input-Output pin; open-drain; requires pull-up resistor.
V _{SS}	6	Ground
INT	7	Interrupt Output pin; open-drain; active LOW
NC	8	Not Connected
NC	9	Not Connected
CLKOE	10	CLKOUT enable/disable pin; enable is active HIGH