

# CXO2H

VCXO Clock Oscillator 5 MHz – 170 MHz



# **DIMENSIONS** Recommended Solder Pad: Package: 5,0 2,6 1,2 1,2 1 4 2 9, 3 pin 1 V<sub>C</sub> pin 2 GND 1,7 max \* pin 3 F<sub>OUT</sub> pin 4 V<sub>DD</sub> All dimensions in mm typical

#### **APPLICATIONS**

Security / Safety Avionics / Aerospace Remote Control / Telemetry Microprocessor and FPGA Clocks Test and Measurement Equipment Wired and Wireless Communications

#### **DESCRIPTION**

The VCXO2H is a Voltage Controlled SMD Oscillator that incorporates an integrated HCMOS circuit together with an XTAL. It operates under vacuum in a hermetically sealed ceramic package.

#### **FEATURES**

Outstanding hermetic sealing with gold-tin preform.

\* also 1,7 max for kovar lid

High stability and low aging guaranteed by hermetic sealing. Wide frequency pulling range APR.

Very fast start-up.

Operates in fundamental mode.

High shock and vibration resistant.

100% Pb-free, RoHS-compliant.

### **ELECTRICAL CHARACTERISTICS** AT 25°C

Minimum Absolute Pull Range APR  For temperature range B = -40 to +85°C	ΔF/F	±130	ppm
C = -55 to +125°C		±130 ±110	
Supply voltage ±5% 2)	$V_{DD}$	3.3	V
Input current	I <sub>DD</sub>	See I <sub>DD</sub> table	
Output signal		HCMOS compatible	
F <sub>OUT</sub> duty cycle @ V <sub>DD</sub> /2 (min./max.)	$\delta_{FOUT}$	40 / 60	%
Rise & fall time $(C_L = 15 \text{ pF}, 20\% \text{ to } 80\% \text{ V}_{DD})$	t <sub>r</sub> / t <sub>f</sub>	≤ 2	ns
Output level V <sub>OL</sub> / V <sub>OH</sub>		< 0.4 / > V <sub>DD</sub> -0.5	V
Start-up time	t <sub>START</sub>	< 5	ms
Capacitive load min. / max.	C <sub>L</sub>	3 / 27	pF

<sup>1)</sup> Including adjustment at +25°C, long term aging 1 year,  $V_{DD}$  variations ±5%, C<sub>1</sub> variations min. to max. and frequency stability over temperature range

<sup>2)</sup> A 47 nF decoupling capacitor has to be connected between  $V_{DD}$  and GND

INPUT CURRENT: I<sub>DD</sub> (no load)

Frequency	≤ 40 MHz	≤ 100 MHz	≤ 130 MHz
$V_{DD} = 3.3 \text{ V (V)}$	< 10 mA	< 20 mA	< 25 mA

#### FREQUENCY CONTROL RANGE

Control voltage range	V <sub>C</sub>	0 to 3.3	V
Slope polarity	positive		
Control voltage bandwidth @ -3 dB	V <sub>CBW</sub>	≥ 10	kHz
V <sub>C</sub> input impedance	Z <sub>VC</sub>	≥ 100	kΩ

## STANDARD FREQUENCIES

Frequencies			
16.3840 MHz	24.5760 MHz	80.0000 MHz	100.0000 MHz
128.0000 MHz			
Other frequencies from 5 MHz to 170 MHz on request			

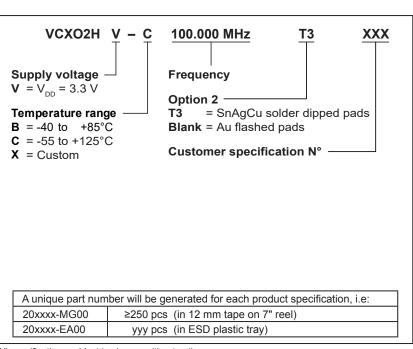
# ENVIRONMENTAL CHARACTERISTICS

	Conditions
Storage temperature range	−65 to +125°C
Shock resistance (survival)	5000 g, 0.3 ms, ½ sine
Vibration resistance (survival)	20 g / 10 – 2000 Hz

# TERMINATIONS AND PROCESSING, OPTION 2

Reflow per IPC/JEDEC J-STD-020C	260°C / 20 - 40 s
Package	Ceramic
Lid	Ceramic Lid
Terminations (Option 2)	SnAgCu solder dipped pads (T3)
	Au flashed pads (Blank)

### **ORDERING INFORMATION**



All specifications subject to change without notice.



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