

MCSO2EU Ultra Low Power

High Temp Clock Oscillator 32.768 kHz



DIMENSIONS Recommended Solder Pad: Package: 5,0 2,6 1,2 1,2 1 4 2 1,6 3 pin 1 E/D pin 2 GND 1,7 max pin 3 F_{OUT} pin 4 V_{DD} All dimensions in mm typical

APPLICATIONS

Security / Safety Avionics / Aerospace Radio Communication Geothermal Equipment Remote Control / Telemetry Down Hole and Well Drilling

DESCRIPTION

The MCSO2EU is a High Temperature, 32.768 kHz SMD Oscillator that incorporates an integrated HCMOS circuit together with an XTAL. It operates under vacuum in a hermetically sealed ceramic package.

FEATURES

Outstanding hermetic sealing with gold-tin preform. High stability and low aging guaranteed by hermetic sealing. Frequency stability guaranteed for 1000 h at T_{MAX}. Very fast start-up.

Operates in fundamental mode.

High shock and vibration resistant.

100% Pb-free, RoHS-compliant.

ELECTRICAL CHARACTERISTICS AT 25°C

Overall frequency stability over 1) temperature range C = -55 to +125°C E = -55 to +150°C D = -55 to +175°C	ΔF/F	≤ ±100 ≤ ±150 ≤ ±300	ppm
Supply voltage ±5% 2)	V_{DD}	2.5 / 3.3	V
Input current	I _{DD}	See I _{DD} table	
Output signal		HCMOS compatible	
F _{OUT} duty cycle @ V _{DD} /2 (min./max.)	δ_{FOUT}	40 / 60	%
Rise & fall time $(C_L = 15 \text{ pF}, 20\% \text{ to } 80\% \text{ V}_{DD})$	t _r / t _f	≤ 25	ns
Output level V _{OL} / V _{OH}		< 0.4 / > V _{DD} -0.5	V
Start-up time	t _{START}	< 5	ms
Capacitive load min. / max.	C _L	3 / 27	pF

¹⁾ Including adjustment at +25°C, long term aging 1000 h at T_{MAX} , V_{DD} variations ±5% and C_L variations min. to max.

²⁾ A 47 nF decoupling capacitor has to be connected between V_{DD} and GND

INPUT CURRENT: I_{DD} ($C_L = 10 pF$)

Frequency	32.768 kHz
$V_{DD} = 2.5 \text{ V (W)}$	< 20 μA
$V_{DD} = 3.3 \text{ V (V)}$	< 20 μA

STANDARD FREQUENCY

Frequency
32.768 kHz
Other frequencies from 15 kHz to 100 kHz on request

ENABLE/DISABLE E/D, OPTION 1

Input level V _{IL} / V _{IH}		$< 0.3 V_{DD} / > 0.7 V_{DD}$	V
Reaction time	t	< 5	ms
Standby current	IDDD	< 2	μA

Pin 1 E/D	Pin 3 F _{OUT}
V _{IH} or open	Output enabled
V _{IL}	Output disabled (Hi-Z)

No E/D function before V_{DD} is set.

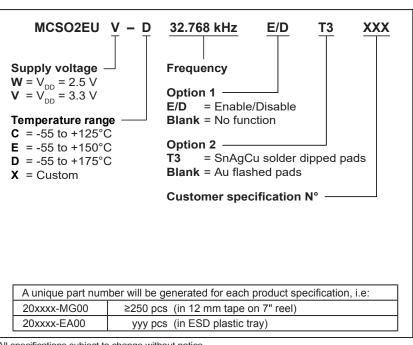
ENVIRONMENTAL CHARACTERISTICS

	Conditions
Storage temperature range	−65 to +125°C
Shock resistance (survival)	10000 g, 0.3 ms, ½ sine
Vibration resistance (survival)	80 g / 10 – 2000 Hz

TERMINATIONS AND PROCESSING, OPTION 2

Reflow per IPC/JEDEC J-STD-020C	260°C / 20 - 40 s
Package	Ceramic
Lid	Ceramic lid
Taminations (Ontion 2)	SnAgCu solder dipped pads (T3)
Terminations (Option 2)	Au flashed pads (Blank)

ORDERING INFORMATION



All specifications subject to change without notice.



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