

MCSO₂E

High Temp Clock Oscillator 15 kHz - 100 MHz



DIMENSIONS Package: Recommended Solder Pad: 5,0 1,2 1,2 1 4 2 9, 3 pin 1 E/D pin 2 GND 1,7 max * pin 3 F_{OUT} pin 4 V_{DD} All dimensions in mm typical * also 1,7 max for kovar lid

APPLICATIONS

Security / Safety Avionics / Aerospace Radio Communication Geothermal Equipment Remote Control / Telemetry Down Hole and Well Drilling

DESCRIPTION

The MCSO2E is a High Temperature, High Frequency SMD Oscillator that incorporates an integrated HCMOS circuit together with an XTAL. It operates under vacuum in a hermetically sealed ceramic package.

FEATURES

Outstanding hermetic sealing with gold-tin preform.

High stability and low aging guaranteed by hermetic sealing. Frequency stability guaranteed for 1000 h at T_{MAX} .

Very fast start-up.

Operates in fundamental mode.

High shock and vibration resistant.

100% Pb-free, RoHS-compliant.

ELECTRICAL CHARACTERISTICS AT 25°C

Overall frequency stability over 1) temperature range	ΔF/F	≤ ±100 ≤ ±150 ≤ ±300 ≤ ±400	ppm
Supply voltage ±5% 3)	V _{DD}	2.5 / 3.3 / 5.0	V
Input current	I _{DD}	See I _{DD} table	
Output signal		HCMOS compatible	
F _{OUT} duty cycle @ V _{DD} /2 (min./max.)	δ_{FOUT}	40 / 60	%
Rise & fall time, \leq 20 MHz For L version, $t_r / t_f \leq$ 25 ns ($C_L = 15$ pF, 20% to 80% V_{DD})	t _r / t _f	≤ 7	ns
Rise & fall time, > 20 MHz (C _L = 15 pF, 10% to 90% V _{DD})	t _r / t _f	≤ 3	ns
Output level V _{OL} / V _{OH}		$< 0.4 / > V_{DD} - 0.5$	V
Start-up time	t _{START}	< 5	ms
Capacitive load min. / max. For L version, C _L max. = 27 pF	C _L	3 / 47	pF

- 1) Including adjustment at +25°C, long term aging 1000 h at T_{MAX} , V_{DD} variations ±5% and C_L variations min. to max.
- 2) For the low consumption version (L), G version is only available as 5.0 V version and the G range is limited to +200°C
- 3) A 47 nF decoupling capacitor has to be connected between V_{DD} and GND

INPUT CURRENT: I_{DD} (no load)

(For L version, $C_L = 10 pF$)

Frequency	32.768 kHz (L)	≤ 10 MHz	≤ 20 MHz	> 20 to 100 MHz
$V_{DD} = 2.5 V (W)$	< 100 µA	< 2 mA	< 3 mA	< 15 mA
$V_{DD} = 3.3 \ V \ (V)$	< 110 µA	< 4 mA	< 5 mA	< 20 mA
$V_{DD} = 5.0 \text{ V (Blank)}$	< 120 µA	< 6 mA	< 7 mA	< 30 mA

STANDARD FREQUENCIES

Frequencies				
32.768 kHz	3.6864 MHz	4.0000 MHz	8.0000 MHz	10.0000 MHz
12.0000 MHz	12.8000 MHz	14.7456 MHz	16.0000 MHz	20.0000 MHz
24.0000 MHz	40.0000 MHz			

L version: Other frequencies from 15 kHz to 100 kHz on request Standard version: Other frequencies from 100 kHz to 100 MHz on request

ENABLE/DISABLE E/D, OPTION 1

Input level V _{IL} / V _{IH}		$< 0.3 V_{DD} / > 0.7 V_{DD}$	V
Reaction time, Standard version	t	< 1	μs
Reaction time, L version	t	< 5	ms

Pin 1 E/D	Pin 3 F _{OUT}
V _{IH} or open	Output enabled
V _{IL}	Output disabled (Hi-Z)

No E/D function before V_{DD} is set.

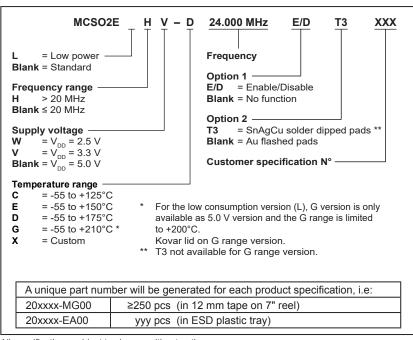
ENVIRONMENTAL CHARACTERISTICS

TERMINATIONS AND	
PROCESSING, OPTION 2	

	Conditions
Storage temperature range	−65 to +125°C
Shock resistance (survival)	10000 g, 0.3 ms, ½ sine
Vibration resistance (survival)	80 g / 10 – 2000 Hz

Reflow per IPC/JEDEC J-STD-020C	260°C / 20 - 40 s
Package	Ceramic
Lid	Ceramic lid (Kovar lid on G range version)
Terminations (Option 2) (T3 not available for G range)	SnAgCu solder dipped pads (T3)
	Au flashed pads (Blank)

ORDERING INFORMATION



All specifications subject to change without notice.



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